

## EAST Search History

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| L1    | 1037 | (threshold\$1 same leak\$3) and (transistor\$1 same gate\$1 same source) and suppress\$3 and driv\$3 and (negative same voltage\$1) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:51 |
| L2    | 974  | (threshold\$1 same leak\$3) and (transistor\$1 same gate\$1 same memory) and suppress\$3 and driv\$3 and (source same voltage\$1)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:52 |
| L3    | 1053 | (threshold\$1 same leak\$3) and (transistor\$1 same gate\$1 same memory) and suppress\$3 and driv\$3 and (gate same voltage\$1)     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:52 |
| L4    | 405  | (threshold\$1 same leak\$3 same suppress\$3) and (transistor\$1 same gate\$1 same memory) and driv\$3 and voltage\$1                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:52 |
| L5    | 216  | (threshold\$1 same leak\$3 same memory) and (transistor\$1 same gate\$1) and suppress\$3 and driv\$3 and (level\$1 same shift\$3)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:52 |
| L6    | 390  | 1 and ("365"/\$.ccls.)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:54 |
| L7    | 570  | 2 and ("365"/\$.ccls.)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:52 |
| L8    | 606  | 3 and ("365"/\$.ccls.)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:52 |
| L9    | 239  | 4 and ("365"/\$.ccls.)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2006/09/26 09:53 |

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|     |     |                        |   |    |     |                  |
|-----|-----|------------------------|---|----|-----|------------------|
| L10 | 184 | 5 and ("365"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L11 | 96  | 1 and ("326"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L12 | 55  | 2 and ("326"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L13 | 59  | 3 and ("326"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L14 | 16  | 4 and ("326"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L15 | 1   | 5 and ("326"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L16 | 145 | 1 and ("327"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L17 | 73  | 2 and ("327"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L18 | 75  | 3 and ("327"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:53 |
| L19 | 26  | 4 and ("327"/\$.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:54 |

## EAST Search History

|     |    |                         |   |    |     |                  |
|-----|----|-------------------------|---|----|-----|------------------|
| L20 | 20 | 5 and ("327"/\$.ccls.)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:54 |
| L21 | 30 | 1 and ("365"/154.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L22 | 55 | 2 and ("365"/154.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:54 |
| L23 | 59 | 3 and ("365"/154.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:54 |
| L24 | 32 | 4 and ("365"/154.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:54 |
| L25 | 3  | 5 and ("365"/154.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:54 |
| L26 | 73 | 1 and ("365"/226.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L27 | 83 | 2 and ("365"/226.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L28 | 85 | 3 and ("365"/226.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L29 | 54 | 4 and ("365"/226.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |

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|     |    |                            |   |    |     |                  |
|-----|----|----------------------------|---|----|-----|------------------|
| L30 | 32 | 5 and ("365"/226.ccls.)    | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L31 | 27 | 1 and ("365"/189.11.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L32 | 35 | 2 and ("365"/189.11.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L33 | 35 | 3 and ("365"/189.11.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L34 | 20 | 4 and ("365"/189.11.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:55 |
| L35 | 16 | 5 and ("365"/189.11.ccls.) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2006/09/26 09:56 |

## EAST Search History

| Ref # | Hits | Search Query   | DBs      | Default Operator | Plurals | Time Stamp       |
|-------|------|--|----------|------------------|---------|------------------|
| L1    | 0    | ((threshold\$1 same leak\$3) and (transistor\$1 same gate\$1 same source) and suppress\$3 and driv\$3 and (negative same voltage\$1)).clm. | US-PGPUB | OR               | OFF     | 2006/09/26 12:10 |
| L2    | 1    | ((threshold\$1 same leak\$3) and (transistor\$1 same gate\$1 same memory) and suppress\$3 and driv\$3 and (source same voltage\$1)).clm.   | US-PGPUB | OR               | OFF     | 2006/09/26 12:10 |
| L3    | 1    | ((threshold\$1 same leak\$3) and (transistor\$1 same gate\$1 same memory) and suppress\$3 and driv\$3 and (gate same voltage\$1)).clm.     | US-PGPUB | OR               | OFF     | 2006/09/26 12:11 |
| L4    | 1    | ((threshold\$1 same leak\$3 same suppress\$3) and (transistor\$1 same gate\$1 same memory) and driv\$3 and voltage\$1).clm.                | US-PGPUB | OR               | OFF     | 2006/09/26 12:11 |
| L5    | 0    | ((threshold\$1 same leak\$3 same memory) and (transistor\$1 same gate\$1) and suppress\$3 and driv\$3 and (level\$1 same shift\$3)).clm.   | US-PGPUB | OR               | OFF     | 2006/09/26 12:11 |